

## Registration Form

**ISTE, New Delhi, Approved Five Days STTP**  
on  
**“Advanced Signal, Image and Video  
Processing on FPGA”**

Full Name:

.....

Name of College/Institution/Organization:

.....

Qualification:.....

Designation:.....

Full Address:.....

.....

Pin code: .....

Phone No.: .....

Mobile No.: .....

E-mail:.....

I have enclosed the registration fee of Rs .....  
by D.D. / Cheque bearing No.....  
drawn in favor of **“Director , Textile and Engineer-  
ing Institute, Ichalkaranji”**

Place:.....

Date : ..... Signature of the participant

### Endorsement by Head of Organization

Date:

Sign and Seal of Principal

## Registration Fee Details

Faculty / Industry Person: 1500/-

ME / P.hD Student : 1000/-

## Important Dates

Receipt of Application :

19<sup>th</sup> June 2017

Confirmation of Selection :

22<sup>th</sup> June 2017

## PATRONS

**Dr. P. V. Kadole**

**Director,**

**Textile & Engineering Institute,  
Ichalkaranji**

## Contact Person and Coordinators

**Prof. (Dr) Mrs L. S. Admuthe**

**Dy. Director (Academics),  
and Head of Electronics Department.  
Mobile no: 09404987166**

**Coordinator:**

**Prof. (Dr) Mrs V Jayashree  
PG Coordinator,  
jayashreevaddin@gmail.com  
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**Co-coordinator**

**B.N.Sachidanand  
Assistant Professor,  
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Department of Electronics Engineering,  
DKTE's Textile and Engineering Institute,

**ISTE, New Delhi, Approved Five Days STTP**

on

**“Advanced Signal, Image and Video  
Processing on FPGA”**  
26<sup>th</sup> to 30<sup>th</sup> June, 2017



**IEEE**



**DKTE**

Promoting Excellence in  
Teaching, Learning & Research



## Organizers

**SAI-TECH, Bengaluru**

and

**IEEE Student Chapter, Dept. of Electronics,  
DKTE's Textile and Engineering Institute,  
Rajwada, Ichalkaranji-416115  
An Autonomous Institute,  
A+ Grade by NAAC**

**www.dktes.com**

## About the Institute

The genesis and growth of the institute was a sequel to the intense desire and support from Mr. K. B. Awade, former member of Parliament & Educationist founded D.K.T.E. Society's Textile & Engineering Institute at Ichalkaranji in 1982. Presently the institute conducts ten degree courses in Textiles, Mechanical Engineering, Computer Science & Engineering, Information Technology and Electronics Engineering, Electronics and Telecommunications Engineering, three diploma courses in Textiles and seven postgraduate courses in textiles as well as Mechanical Engineering and Electronics Engineering. The total student strength of the institute is around 3000 and has 186 teaching faculty members and 285 non-teaching staff. The management has provided excellent infrastructural facilities and a highly qualified faculty. As a result of this, very high standard of education is maintained. Apart from regular teaching this institute is well known for its interaction with industry. Institute is affiliated to and also has SUK recognized research center for Textiles, Electronics & mechanical. DKTE has received NAAC A+ accreditation and ISO 9001:2008 certified. Also, DKTE has received permanent affiliation from Shivaji University Kolhapur.

## About the Department

Department of Electronics Engineering is established in the year 1988-89. The Department has well equipped laboratories in the major area of Electronics Engineering like Digital Electronics, Communication Engineering,

Digital Signal Processing, Embedded System, VLSI Design, Power Electronics and Computer Network etc. The exclusive computing facility includes software tools like Keil, PSPICE, ORCAD, Xilinx, Vivado, Matlab. The department has research lab equipped with Xilinx Spartan-6, Atlys and Anvyl, Modrob sponsored multidimensional Lab and Cypress embedded processors Lab from ARM University.

## Course Contents

### “Advanced Signal, Image and Video Processing on FPGA”

- VLSI architectures for signal processing with suitability for FPGA implementation
- Filter Design : Method of creating efficient FIR filters in Xilinx Device.
- Introduction to Image Processing and Video Processing Application
- Advanced FPGA Architecture (VIRTEX 4)
- RTL Design Concepts using HDL & Testbench
- Image Processing Tool Box & Preprocessing Filter Design
- Image Filter Design- 2<sup>nd</sup> order Gaussian using HDL
- Image Filter Design - 2<sup>nd</sup> order Gaussian Test Bench Generation & Verification.
- Timing Closures for proper Design synchronization
- Advanced Power Estimation & Optimization
- Debugging using chip scope pro on FPGA

- System Generator Design Flow using SIMULINK.
- Xilinx System Generator Black Box creation & synchronization for FPGA
- Xilinx System Generator configuration & Implementation on Real time FPGA board.
- Moving Object Detection in Video - Example

## Resource Persons

1. Mr. Sayantam and group SaiTech group, Bangalore
2. Dr.S.Subbaraman, Walchand College of Engineering, Sangli
3. Dr. R.M.Bankar, KLE Technological University, Hubballi, Karnatak.
4. Dr.V.Jayashree, Textile & Engineering Institute, Ichalkaranji

## Eligibility

- \* Teachers from various streams of science, engineering and polytechnics colleges.
- \* Working professionals from Industries, R&D organizations
- \* PG /PhD students

[www.dktes.com](http://www.dktes.com)